

ATRIE 4E1OIP

ETHERMUX

ATRIE - MULTI-4E1/T1 OVER IP

Evolutionary Convergence in Transmission of Traditional Voice and Data over IP Network

PRODUCT OVERVIEW

Atrie EtherMux is designed as a multi-service access platform for PDH over IP applications. E1/T1 frames can be mapped/de-mapped into/from IP packets. An adaptive clock recovery method for Ingress PDH (PSN -> TDM) clock generation is implemented to support E1/T1 (ITU-T G.823/ANSI T1.105) Jitter performance.

COST-EFFECTIVE IP DEPLOYMENT (PDH OVER IP)

Atrie EtherMux provides cost-effective applications of traditional circuit-switched system over IP. With Atrie EtherMux, it is easy to interconnect existing phone systems over IP that are used to carry data, voice and video.

2G/3G BACKHAUL DEPLOYMENT

With high precision clock recovery technology, Atrie EtherMux is capable of supporting 2G/3G backhaul and provides smooth services.

TRANSPARENT TRANSMISSION

Atrie EtherMux can transparently transport proprietary signaling that are required to support PBX features, including call conference, call forwarding and SS7. Customer can easily apply and enjoy better integration of TDM and IP devices with lower network expense.

BYPASS INTERNATIONAL TOLL

With a pair of Atrie EtherMuxes and guaranteed internet bandwidth, it is sure to save cost dramatically, and to ensure the QoS of voice based on

FEATURES

- Support IEFT RFC4533 Structure-Agnostic TDM over Packet (SATO P), Metro Ethernet Forum MEF8.
- 4 x E1/T1 NRZ Serial Interface with LOS/AIS detection
- Use Raw Encapsulation method for PDH payload over IP packet.
- Support Circuit Emulation Service over IP.
- Comply with IEFT draft standard for CESoPSN and SAToP; Metro Ethernet Forum MEF8 IA.
- Support both Point-to-Point and Point-to-Multipoint operation.
- Support 4 independent Adaptive Clock recovery block for Ingress PDH (PSN -> TDM) clock generation. Recovered clock jitter is compliant to ITU-T G.824/ANSI T1.105 (E1/T1 Jitter Control).
- Independent configurable jitter buffer depth to compensate up to 250ms of Packet Delay Variation.
- Lost packets processing/compensation via PW (Pseudo Wire) control field Sequence Number.
- Provide Subscriber side Data traffic bandwidth control to guarantee enough TDM payload bandwidth.
- PDH LOS detection triggered PW L field or payload AIS generation at Egress direction (TDM -> PSN).
- Configurable IEEE 802.3 DA/SA assignment.
- Configuration can be made through RS-232 console port or Easy-to-use GUI (Graphic User Interface).



SPECIFICATION

USER INTERFACE (CPE SIDE)

Port: up to 4 x E1/T1 (ITU-T G.703/ANSI T1.102)
Interface: RJ-48c (120 Ohm for E1, 100 Ohm for T1)
Line Coding: HDB3 (E1), B8ZS (T1)

ETHERNET INTERFACE (CPE / CO SIDE)

Port: 100 Base-T Ethernet
Interface: RJ-45

DIMENSIONS

H x W x D: 44.5 x 242 x 302 (mm)

MAIN POWER SUPPLY

AC: 110 ~ 240V @ 47 ~ 65Hz
DC: -72V ~ -36V (Option)

ENVIRONMENT CONDITION

Ambient temperature: 0°C ~ 55°C
Storage temperature: 0°C ~ 85°C
Relative humidity: 5 ~ 95% non condensing

CONFIGURATION AND MANAGEMENT

RS-232 console port (Craft Terminal)
or SNMP-based management

ORDERING INFORMATION

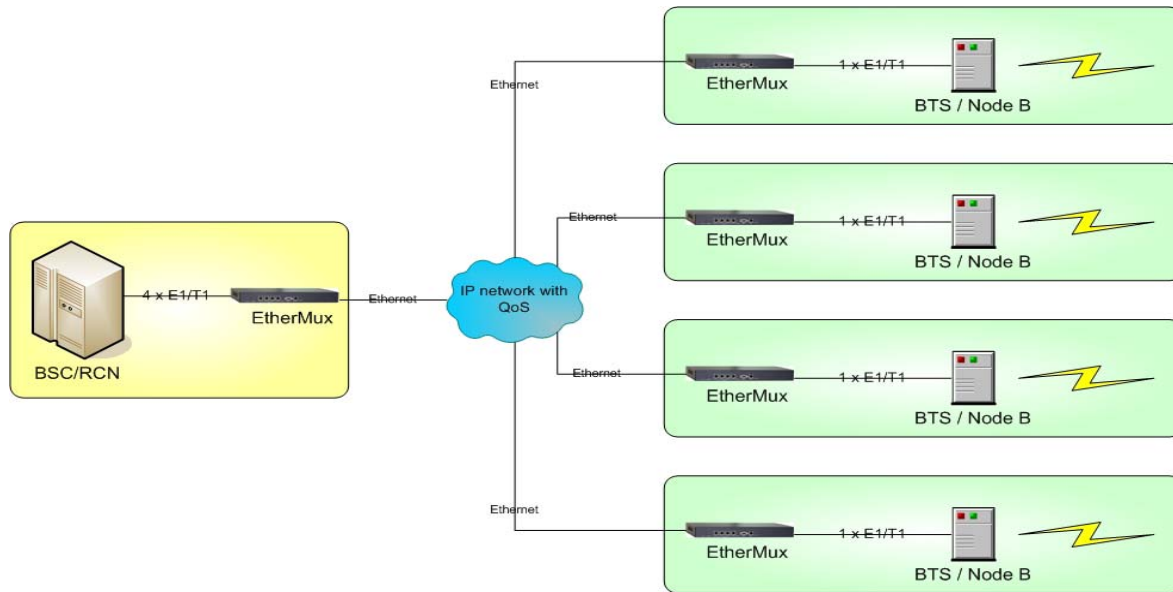
Feature Options:

[**CPE Side Interface**] 2x E1/T1 4x E1/T1

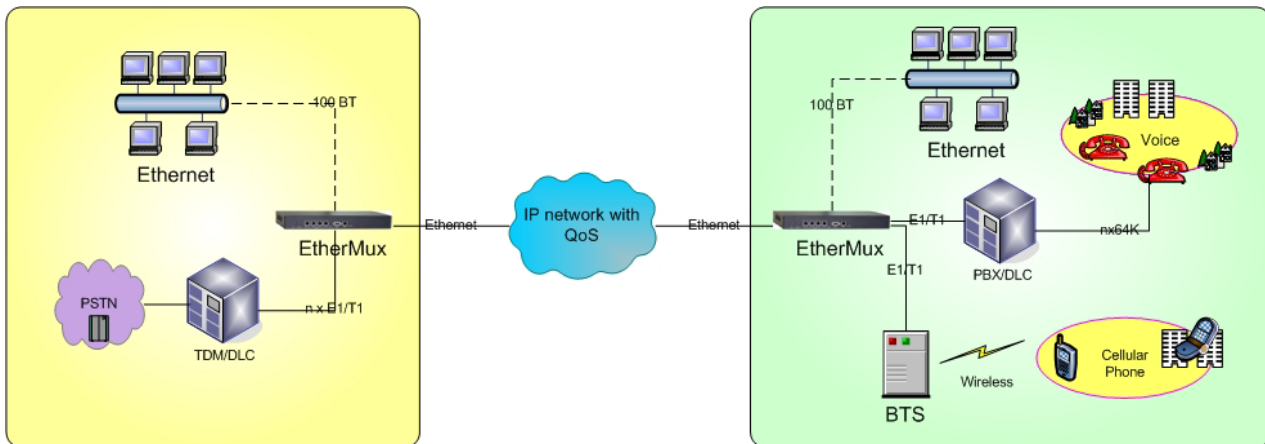
[**Management**] Craft terminal Craft terminal + SNMP-based GUI management

[**Power**] 1x AC 1x AC + 1x DC

ETHERMUX APPLICATION



2G/3G backhaul application



Circuit Emulation Service over IP

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